



6TH INTERNATIONAL CONFERENCE
on
**EMERGING TECHNOLOGIES: MICRO TO
NANO (ETMN-2024)**



Jointly Organized by
Department of Electrical Engineering, Faculty of Engineering &
Technology, Jamia Millia Islamia, New Delhi & Manipal University
Jaipur

Dr. Dhawal Gupta

**Present Designation: Group Business Director, Chase India | Government Policy
Strategist | Ex-Cyber Law
Ministry of Electronics & IT (MeitY)**



Dr. Dhawal Gupta is an innovative tech policy strategist and government advisor with over 23 years of experience in bridging technology, governance, and public policy. Currently, as Group Business Director at Chase India, he leverages his expertise to drive forward-thinking policy solutions in the digital and tech domains. Previously, he served as Head of Cyber Policy at the Ministry of Electronics & IT (MeitY), where he played a pivotal role in shaping India's cyber policies and e-governance standards.

Dr. Gupta holds a Ph.D. in AI applications for earthquake prediction from IIT Delhi and is certified in European data privacy (CIPP/E). He has been actively involved in global cyber initiatives, liaising with the Global Forum for Cyber Expertise and mentoring doctoral research at Jamia Millia Islamia. His work continues to advance India's position in the global digital landscape through innovative public-private partnerships and policy frameworks.



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Dr. Ralf Butcher

Present Designation: Regional Manager Germany
H&MV Engineering



Dr. Ralf Butcher is a seasoned expert in electrical power engineering and project management, currently serving as Regional Manager for Germany at H&MV Engineering. With a focus on EPC services for 380 kV and 110 kV substations—primarily for hyperscale data centres and grid-scale Battery Energy Storage Systems (BESS)—Ralf brings a wealth of expertise to high-stakes energy infrastructure projects.

Ralf's career began in power engineering with Voith Siemens Hydro, where he commissioned turbine-generator units in Malawi and Kenya, gaining hands-on experience in power plant operations. He then advanced to chief electrical engineer at Lahmeyer International (now Tractebel Engineering), where he oversaw critical high-voltage projects, including the testing and initial energization of the €600 million Sudanese grid for the Merowe Dam.

An academic achiever with an MSc in environmental engineering and a PhD from the University of Edinburgh, Ralf has published extensively on topics like smart grids and BESS. He is also an active member of the IEC/TC4 standardization team in Germany, contributes as a peer reviewer for scientific journals, and regularly participates in international energy panels.

In his role at H&MV Engineering since 2023, Ralf leads initiatives to advance Germany's power grid infrastructure, drawing from over 48-country experience and expertise in languages and cross-cultural project management.



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Dr. Joby Antony

Present Designation: Engineer-F
Inter University Accelerator Centre (IUAC)



Dr. Joby Antony is an expert in Interfacing, Automation, and Computer Controls (IAUC) with over 25 years of experience, currently serving as Engineer-F at the Inter University Accelerator Centre (formerly Nuclear Science Centre). His distinguished academic and professional journey includes a Bachelor's degree in Electronics and Communication Engineering from JMI, New Delhi, where he graduated at the top of his class, and a Master's in Electronics & Computer Technology from Kent State University, Ohio, USA. He earned his Ph.D. from IIT (ISM) Dhanbad.

Dr. Antony has also worked as a Visiting Scientist at CERN, Geneva, contributing to the Large Hadron Collider Instrumentation during its commissioning phases in 2004 and 2014. As the current head of Electronics for Cryogenics at IUAC, he continues to advance innovations in scientific instrumentation and automation within nuclear research.



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Mr. Masood Ahmed Syed

Present Designation: Chief General Manager (Production), BrahMos Aerospace



Syed Masood Ahmed is an experienced production leader with over 30 years in the defense and aerospace industry. Currently the Chief General Manager of Production at BrahMos Aerospace. He oversees unit operations, article production, integration testing, and product support. His extensive background includes a variety of managerial roles, leading teams in systems engineering, lean manufacturing, and Six Sigma methodologies.

Prior to his current role, he served as Additional General Manager and Deputy General Manager, with significant experience in manufacturing and assembly at Bharat Dynamics Limited, where he managed precision machine shops and assembly lines for anti-tank guided missiles. He holds a Master's degree in Industrial Engineering and Management from JNTU and a Bachelor's degree in Mechanical Engineering from Osmania University.



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Dr. Vikas Pendem

Present Designation: Compact Device Modeling Engineer, Intel Corporation



Dr. Vikas Pendem is a Compact Device Modeling Engineer at Intel Corporation, specializing in advanced semiconductor device modeling. With a Ph.D. in Microelectronics from IIT Bombay, Dr. Pendem's research focused on III-Nitride nanostructures for optoelectronic and electronic applications. His expertise encompasses the design, fabrication, and efficiency optimization of semiconductor devices, with specific emphasis on gallium nitride LEDs.

Dr. Pendem has been an invited speaker at multiple IEEE and OSA events, presenting on topics such as energy efficiency in LEDs and nanostructure characterization. His prior roles include Teaching Assistant at IIT Bombay and Trainee Scientist at CSIR-CEERI, where he conducted significant research on light-emitting diodes. He is also a recipient of the Visvesvaraya PhD Fellowship awarded by the Ministry of Electronics & IT, India.