Name: Neelofer Afzal

Designation: Professor

Nationality: Indian

Mailing Address: Department of Electronics and Communication Engineering Faculty of Engineering &Technology Jamia Millia Islamia New Delhi

Telephone number: 011-26981284

Field of Specialization: Analog Signal Processing/ Signal Generation

Education: **B.Tech.** in Electronics Engineering in 1997.
Z. H College of Engineering & Technology
Aligarh Muslim University

M.Tech. in Circuits and Systems in 2000.
Z. H. College of Engineering & Technology
Aligarh Muslim University

PhD from Jamia Millia Islamia in 2009.
(Topic" Digitally Programmable Analog Signal Processing Circuits using CCII")

Teaching/Administrative Experience

Professor (2018-till date): Department of Electronics & Communication Engineering, Faculty of Engineering & Technology, Jamia Millia Islamia, New Delhi

Associate Professor (2015 – 2018):	Department of Electronics & Communication Engineering, Faculty of Engineering & Technology, Jamia Millia Islamia, New Delhi
Assistant Professor (2002-2015):	Department of Electrical Engineering, Faculty of Engineering & Technology, Jamia Millia University New Delhi

Current Administrative Responsibility:

Member –Internal compliment committee

Senior warden- Begum Hazrat Mahal Hall of Girls' Residence

International Journals

1. Anu Tonk, **Neelofer Afzal**, "Ultra-Low Voltage Operable Bulk-Driven Second Generation Current Conveyor Based Filters With Single-Input And Single-Output." Journal of Engineering Science and Technology Vol. 14, No. 1, 2019, pp. 216-226.(Scopus, ESCI)
2. Anu Tonk, **Neelofer Afzal**, "Second generation fully differential current conveyor based analog circuits."Journal of Semiconductors Vol. 40, No. 4: 042401, 2019. (Scopus, ESCI)
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4. Charu Rana, **Neelofer Afzal**, Dinesh Prasad, "A high performance grounded voltage controlled positive Resistor", Journal of Engineering Technology. (SCIE)
5. Charu Rana, **Neelofer Afzal**, Dinesh Prasad, "A Low Voltage Low Power High Performance FGMOS based Current Mirror", *Contemporary Engineering Sciences*, Vol. 10, no. 6, 263 – 271, 2017. (Scopus) JOURNAL
6. Charu Rana, **Neelofer Afzal**, Dinesh Prasad, "High performance voltage controlled positive resistor", *Journal of Engineering and Technology*, 2017.
7. Charu Rana, **Neelofer Afzal**, Dinesh Prasad, "A Simple FGMOS Based Programmable Resistor Simulator and Its Application", *International Journal ofIntelligent Systems and Applications*.(Accepted)
8. D. Singh and **N. Afzal**, "Digitally programmable mixed mode universal filter using followers-A minimal realization," Analog Integrated Circuits& Signal Processing (Springer), Published online 26th Nov. 2015. DOI 10.1007/s10470-015-0664-2 (IF-0.468)

9. **N. Afzal**, "Digitally Programmable Versatile Grounded Multiplier using CCII",International Journal of Engineering research and Development Volume 13 issue 2 February 2017, pp. 62-66
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12. D. Singh and **N. Afzal**, "Fully digitally programmable voltage mode universal filter," Analog Integrated Circuits & Signal Processing (Springer), vol. 81, no. 3, pp. 741-750. Dec. 2014. DOI 10.1007/s10470-014-0418-6 (IF-0.468)
13. **N. Afzal** and D. Singh, "Reconfigurable Mixed Mode Universal Filter," Active and Passive Electronic Components (Hindawi Publishing corporation), Article ID 769198, pages-14, 2014. DOI:10.1155/2014/769198 (Scopus Indexed)
14. D. Singh and **N. Afzal**, "Digitally Programmable Current Conveyor Based Mixed Mode Universal Filter, "International Journal of Electronics Letters (Taylor & francis) vol. 3, no. 3, pp. 170-185, 2015. DOI: 10.1080/21681724.2014.917714 (IF-0.459)
15. D. Singh and **N. Afzal**, "Digitally programmable high-Q voltage mode universal filter," Radioengineering, vol. 22, no. 4, pp. 995-1006, Dec. 2013. (IF-0.653)
16. **N.Afzal**, "Digitally Programmable Floating Impedance Multiplier Using DVCC," International Journal of Electronics Comm. and Computer technology. Issue 1, Vol. 3, Jan 2013.
17. **N. Afzal**, I.A. Khan, "Digitally Programmable Voltage Mode Quadrature Oscillator Using Current Conveyors" International Journal of Engineering research and Development Pp 55-61 vol. 5, Issue 8, Jan 2013.
18. Archna Agarwal, **N. Afzal**, Characteristics of Power Line Channel & Their Effects on Designing of Communication System. International journal of Advanced Engineering & Applications Pp. 135-137. Jan 2011.
19. A. Khan, **N. Afzal**, M. R. Khan, "Digitally Programmable Impedance Multiplier using CCIIs with High Resolution Capability,". Journal of Active and Passive Electronic Devices (USA) Vol. 8, pp - 247-257, 2009
20. A. Khan, M. R. Khan, **N. Afzal**, "Digitally Programmable multifunctional current mode filter using CCIIs," Journal of Active and Passive Devices. (USA) Vol.1, pp.-213-220, 2006

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2. I. A. Khan, **N. Afzal**, M. R. Khan, "Digitally Programmable Generalised Impedance Multiplier Using CCII". Proc. International Conference on Robotics, Vision, Information and Signal Processing- ROVISP (Nov. 28-30)2007, pp. 322-324, Penang Malaysia.

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4 Charu Rana, **Neelofer Afzal**, "Advances in sensor networks using analog signal processing circuits" Recent Advances in Engineering and Computational Sciences(RAECS), 2014Date of Conference: 6-8 March 2014 IEEE

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1. I.A. Khan, **N. Afzal**, M. R. Khan, "Digitally Programmable current mode four phase quadrature oscillator using CCIIs with high resolution capability", National Conference on Modern Trends in Electronics and Communication Systems. Pp. 207-209, March 8-9. 2008 (MTECS-2008). Organised by Department of Electronics. Faculty of Engg & Tech. AMU and IETE Aligarh India.

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Book Chapter

1. Charu Rana, **Neelofer Afzal**, Dinesh Prasad, Anu "Low voltage low power FGMOS based third generation current conveyor", *Book Chapter in Advancesin Power Systems and Energy Management ETAEERE 2016*,Springer.(Scopus)