

Time Table For Session 2024-25
B. Tech. (ECE) Odd Semesters
With Effect From: 19.07.2024

Day	Period	I	II	III	IV	01:00-02:00	V	VI	VII	VIII
	Time Class	9:00-10:00	10:00-11:00	11:00-12:00	12:00-01:00		02:00-3:00	03:00-04:00	04:00-05:00	05:30-06:00
M O N	VII	ECS-704: DC MA		ECS-702: WC DNA	ECS-703: ESD (ZS)	L	ECS-711: VLSI Lab (X-1)	(X-1)	(SAL/ANK)	
	V	ECS-504: DCS (WA)		xxxxxxx	ECS-502: CA (NA)		ECS-712: Minor Project (X-2)	(X-2)	INA/DP)	
	III	ECS-302: AE-I (MN)		ECS-306: UHV (AK)	ECS-304: SS (ANK)		ECS-511: Transducer Lab (X-1)	(X-1)	(SIA/RS2)	
T U E	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703:ESD (ZS)	U	ECS-512: Dig tal Circuits Lab (X-2)	(X-2)	(VA/MA)	
	V	ECS-504: DCS (WA)		xxxxxxx	ECS-502: CA (NA)		ECS-310 EDC-I (X-1) DNA/ ZS			
	III	ECS-305: LD (MTB)		ECS 306 UHV (AK)	ECS-304: SS (ANK)		ECS-311 CAS (X-2) (IK/RS3)			
W E D	VII	ECS-704: DC MA		ECS-702: WC DNA	ECS-703:ESD (ZS)	N	ECS-312 Logic Design Lab (X-3)	(X-3)	(AK/RS1)	
	V	ECS-501: AFSP (DP)		xxxxx	ECS-502: CA (NA)		ECS 711: VLSI Lab (X-3)	(X-3)	(SAL/ANK)	
	III	ECS-305: LD (MTB)	ECS-302: AE-I (MN)	ECS-301: EM-III (SJ-App. Sc.)	ECS-306: UHV (AK)		ECS-712: Minor Project (X-1)	(X-1)	(MTB/MU)	
T H U	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	C	ECS-511: Transducer Lab (X-2)	(X-2)	(SIA/RS2)	
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		ECS-512: Digital Circuits Lab (X-3)	(X-3)	(WA/MA)	
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			ECS-310: EDC-I Lab (X2) DNA/ZS)			
F R I D A Y	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	H	ECS-311: CAS Lab (X-3) IK/RS3)			
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		ECS-312: Logic Design Lab (X-1)	(X-1)	(AK/RS1)	
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			ECS-712: Minor Project (X-3)	(X-3)	NA	
S A T U R D A Y	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	B	ECS-511: Transducer Lab (X-3)	(X-3)	(SIA/RS2)	
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		ECS-512: Digital Circuits Lab (X-1)	(X-1)	(VA/NF)	
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			ECS-310: EDC-I Lab (X-3) DNA/ZS)			
S U N D A Y	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	R	ECS-311: CAS Lab (X-1) IK/RS3)			
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		ECS-312: Logic Design Lab (X-2)	(X-2)	(AK/RS1)	
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			ECS-712: Minor Project (X-1)	(X-1)	AK	
M O N D A Y	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	E	ECS-712: Minor Project (X-3)	(X-3)	NA	
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		ECS-511: Transducer Lab (X-3)	(X-3)	(SIA/RS2)	
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			ECS-512: Digital Circuits Lab (X-1)	(X-1)	(VA/NF)	
T U E S D A Y	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	A	ECS-310: EDC-I Lab (X-3) DNA/ZS)			
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		ECS-311: CAS Lab (X-1) IK/RS3)			
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			ECS-312: Logic Design Lab (X-2)	(X-2)	(AK/RS1)	
W E D N E S D A Y	VII	ECS-701: VLSI (SAL)		ECS-702: WC DNA	ECS-703: ESD (ZS)	K	ECS-712: Minor Project (X-1)	(X-1)	AK	
	V	ECS-503: CS (AK)		xxxxx	ECS-502: CA (NA)		xxxxxxx			
	III	ECS-303: CAS (SH)		ECS-301: EM-III (SJ-APPL. SC.)			xxxxx		xxxxxxx	

S. Ananth Kumar Arin

[Signature] 19/7/24

Department of Electronics & Communication Engineering
Time Table For Session 2024-25

B. Tech. (ECE) Odd Semesters

With Effect From: 19.07.2024

F R I	VII	ECS-712 : Minor Project (X-2)		ECS-711: VLSI Lab (X-2) (MN/ANK)
				ECS-712: Minor Project (X-3) (MTB/MU)
	V	ECS-501: AFSP (DP)	ECS-503: CS (AK)	XXXXXXXX
	III	ECS 303: CAS SH	ECS-304: SS (ANK) ECS-313 (X-1) (SH)/DNA	ECS-313 (X-2) ZS/SH ECS-313 (X-3) ZS/SH

SUBJECT CODE	SUBJECT NAME	Faculty
ECS-301	Engineering Mathematics III (EM-III)	SJ-App.
ECS-302	Electronic Devices and Circuits-I (EDC-I)	MN
ECS-303	Circuit Analysis & Synthesis (CAS)	SH
ECS-304	Signals and Systems (SS)	ANK
ECS-305	Logic Design (LD)	MTB
ECS-306	Universal Human Values (UHV)	AK
ECS-501	Active filters and signal processing (AFSP)	DP
ECS-502	Computer Architecture (CA)	NA
ECS-503	Control Systems (CS)	NF
ECS-504	Digital Circuits and systems (DCS)	WA
ECS-701	VLSI	SAL
ECS-702	Wireless Communication (WC)	DNA
ECS-703	Embedded System Design (ESD)	ZS
ECS-704	Digital communication (DC)	MA

S. Intekhab Amin

(Dr. S. Intekhab Amin)
 Incharge-Time Table

Faculty Names and Abbreviations	
Name of Faculty	Abbreviations
Prof. M. T. Beg	MTB
Prof. Mainuddin	MU
Prof. Sajad Ahmad Loan	SAL
Prof. Neelofer Afzal	NA
Prof. Dinesh Prasad	DP
Prof. M. Nizamuddin	MN
Dr. Amber Khan	AK
Dr. Md. Waseem Akram	WA
Dr. S. Intekhab Amin	SIA
Dr. Imran Ahmad Khan	IK
Dr. Mohd Asraf	MA
Dr. Anam Khan	ANK
Dr. Zeenat Shareef	ZS
Dr. Dil Nashin Anwar	DNA
Dr. Shamsul Haq	SH
New Faculty	NF

Sajad A. Loan
 5/8/24
 (Prof. Sajad A. Loan)

Department of Electronics & Communication Engineering

Time Table For Session 2024-25

B. Tech. (ECE) Odd Semesters

With Effect From: 19.07.2024

LAB CODE	LAB NAME	Faculty
ECS-310	EDC-I	DNA(6)/ZS(6)
ECS-311	Circuit Analysis & Synthesis	IK(6)/RS3(6)
ECS-312	Logic Design	AK(6)/RS1(6)
ECS-313	Electronic Workshop	(SH)(6)/DNA(2)/ZS(4)
ECS-511	Transducers	SIA(12)/RS2(12)
ECS-512	Digital Circuits	WA (12)/ MA(8)/NF(4)
ECS-711	VLSI	SAL(8)/MN(4)/ANK(12)
ECS-712	Minor project	All Faculty

Mr. Mohammad Asif (RS1)	ECS-312
Mr. Zuber Rasool (RS2)	ECS-511
Mr. Zafar Alam (RS3)	ECS-311

S. Intekhab Amin

(Dr. S. Intekhab Amin)
Incharge-Time Table

Sajad A. Loan
5/8/24
(Prof. Sajad A. Loan)
Head

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