

Time Table for the year 2019 (Odd Semester)
B.Tech (Electronics & Communication Engg.) w.e.f. 25.08.2019(Revised)

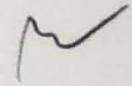
Day	Period Time	I	II	III	IV	1:00 – 02:00	V	VI	VII	VIII
		9:00-10:00	10:00-11:00	11:20-12:00	12:00-01:00		02:00-3:00	3:00-4:00	4:00-5:00	5:00-6:00
M O N D A Y	VII SEM	ECS-704 ←→	AA ←→	ECS-703 ←→	ZS ←→					
	V SEM	ECS-504 ←→	WA ←→	ECS-502 ←→	NA ←→		ECS-511, ECS-512 ←→	(X-1) SIA/ ZS (X-2)WA/ DN		
	III SEM	ECS-301 ←→	App. Sci. ←→	ECS-305 ←→	MTB,R.No.332 ←→		ECS-310 ECS-311 ECS-312 ←→	(X-1) (X-2) (X-3)	NJ/MA AK/SNA GS/DP	
T U E S D A Y	VII SEM	ECS-702 ←→	MNI ←→	ECS-702 ←→	MA ←→		ECS-711 ECS-712 ←→	(X-1) NU/ (X-2) MTB/	MNI MU /DP/NA	
	V SEM	ECS-511ECS- 512 ←→	(X-2) SIA/ ZS (X-3)WA/ DN							
	III SEM	ECS-302, NU ←→	(R.No-330) ←→	ECS-305MT ←→	(R.No-330) ←→		ECS-310 ECS-311 ECS-312 ←→	(X-2) (X-1) (X-3)	MA/NJ AK/SNA DN/GS	
W E D N E S D A Y	VII SEM	ECS-704 ←→	AA ←→	ECS-702 ←→	MA ←→		ECS-711 ←→	(X-2) NU	/MNI	
	V SEM	ECS-503 ←→	AK ←→	ECS-501 ←→	DP ←→		ECS-511 ECS-512 ←→	(X-3) SIA/ ZS (X-1)WA/MNI		
	III SEM	ECS-304,SNA ←→	(R.No-332) ←→	ECS-303 ,GS ←→	(R.No-332) ←→					
T H R U S D A Y	VII SEM	ECS-701 ←→	MNI ←→	ECS-703 ←→	ZS ←→		ECS-711 ECS-712 ←→	(X-3) NU/ (X-1) MTB/	MNI SAL/DP/SIA	
	V SEM	ECS-504 ←→	WA ←→	ECS-502 ←→	NA ←→					
	III SEM	ECS-304 ←→	SNA ←→	ECS-301 ←→	App. Sci. ←→		ECS-310 ECS-311 ECS-312 ←→	(X-3) (X-2) (X-1)	NA/MA AK/NJ GS/DN	

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F R I D A Y	VII SEM						ECS-712	(X-3)NA/AA	MU/AK	
	V SEM	ECS-501	DP	ECS-503	AK					
	III SEM	ECS-303	GS	ECS-302	NU					

N. Afzal

(Prof. Neelofer Afzal)
 Timetable Incharge

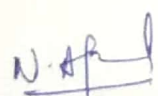


(Prof. M.T. Beg)
 HOD


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SUBJECT CODE	SUBJECT NAME
ECS-301	Engineering Mathematics
ECS-302	Analog Electronics-I
ECS-303	Circuit Analysis
ECS-304	Principles of measurement and Machines
ECS-305	Logic Design
ECS-501	Active filters and signal processing
ECS-502	Computer Architecture
ECS-503	Control Systems
ECS-504	Digital Circuits and systems
ECS-701MEC-351	VLSI
ECS-702	Wireless Communication
ECS-703	Embedded System Design
ECS-704	Digital communication

LAB CODE	LAB NAME
ECS-310	Analog Electronics-I
ECS-311	Logic Design
ECS-312	Circuit Simulation
ECS-510	Transducers
ECS-512	Digital Circuits
ECS-711	VLSI
ECS-712	Minor project


 (Prof. Neelofer Afzal)
 Time Table Incharge

NAME OF THE FACULTY	
Prof. M. T. Beg	MTB
Prof Mainuddin	MU
Prof. Anwar Ahmad	AA
Prof. S. A. Lone	SAL
Prof. Neelofer Afzal	NA
Prof. Dinesh Prasad	DP
Dr. Nizamuddin	NU
Mrs Amber Khan	AK
Dr. WasimAkram	WA
Dr. Syed Intekhab Amin	SIA
Mr. MunshiNoorul Islam	MNI
Mr. Danish Nadeem	DN
Prof. S. Naseem Ahmed	SNA
Mohd. Ashraf	MA
MsGarima Shukla	GS
MsZeenatShareef	ZS
MsNusrat Jahan	NJ


 (Prof. M.T. Beg)
 Head